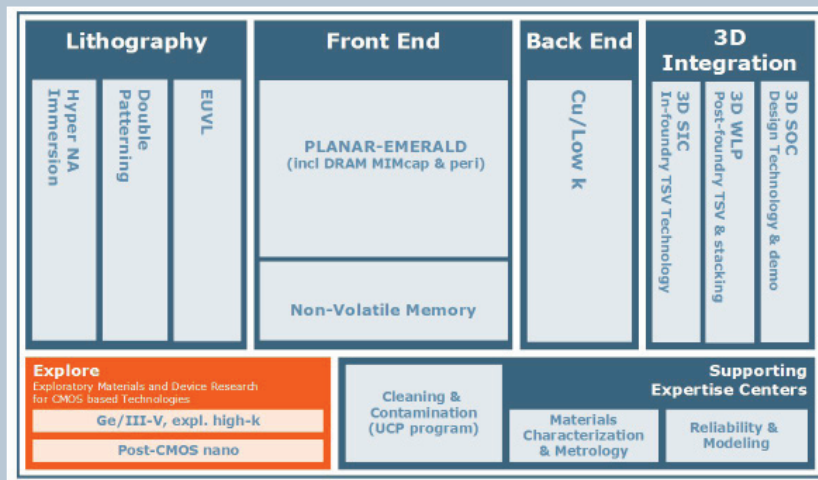


Explore

Positioning

This IIAP is part of IMEC's R&D platform towards sub-32nm CMOS:

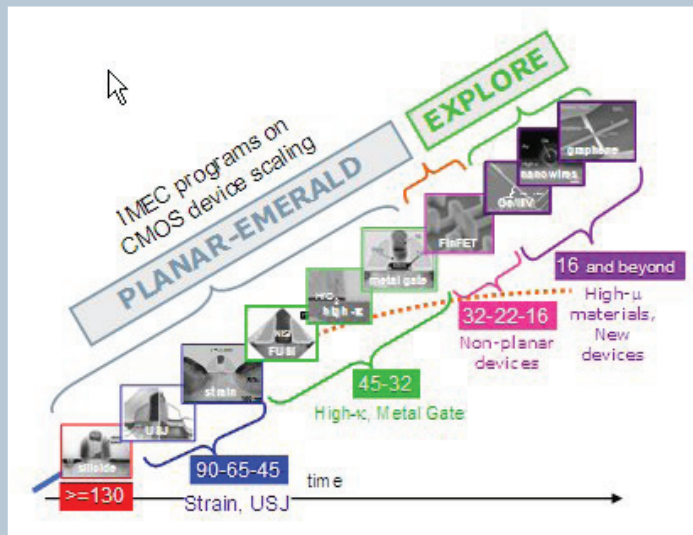


Scope

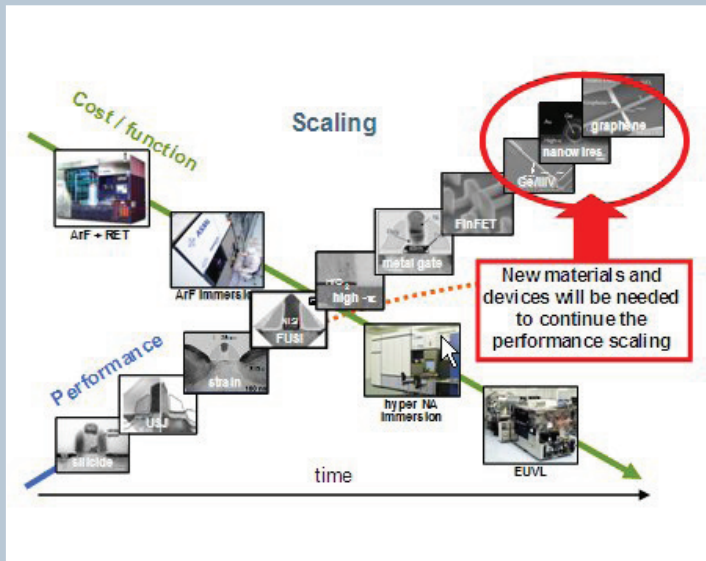
The explore program focuses on the n+3 technology generation. Its mission is to explore and demonstrate the feasibility of introducing new materials and new devices on Si or 'Si-like' wafers for advanced technologies, with the purpose to continue the performance improvement and support new functionalities keeping manufacturability in mind.



The explore program is positioned towards the n+3 technology, beyond the scope of current PLANAR-EMERALD:



Its focus is on new materials and device concepts:



Structure & Activities

The Explore program includes research on various work areas:

- Ge and III/V devices for ultimate CMOS;
- modeling and simulation of new materials and devices;
- nanowires (grown and etched) for 3D transistors;
- new devices concepts;
- carbon nanotubes (CNTs) for interconnects;
- exploration of new materials, e.g., graphene.



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